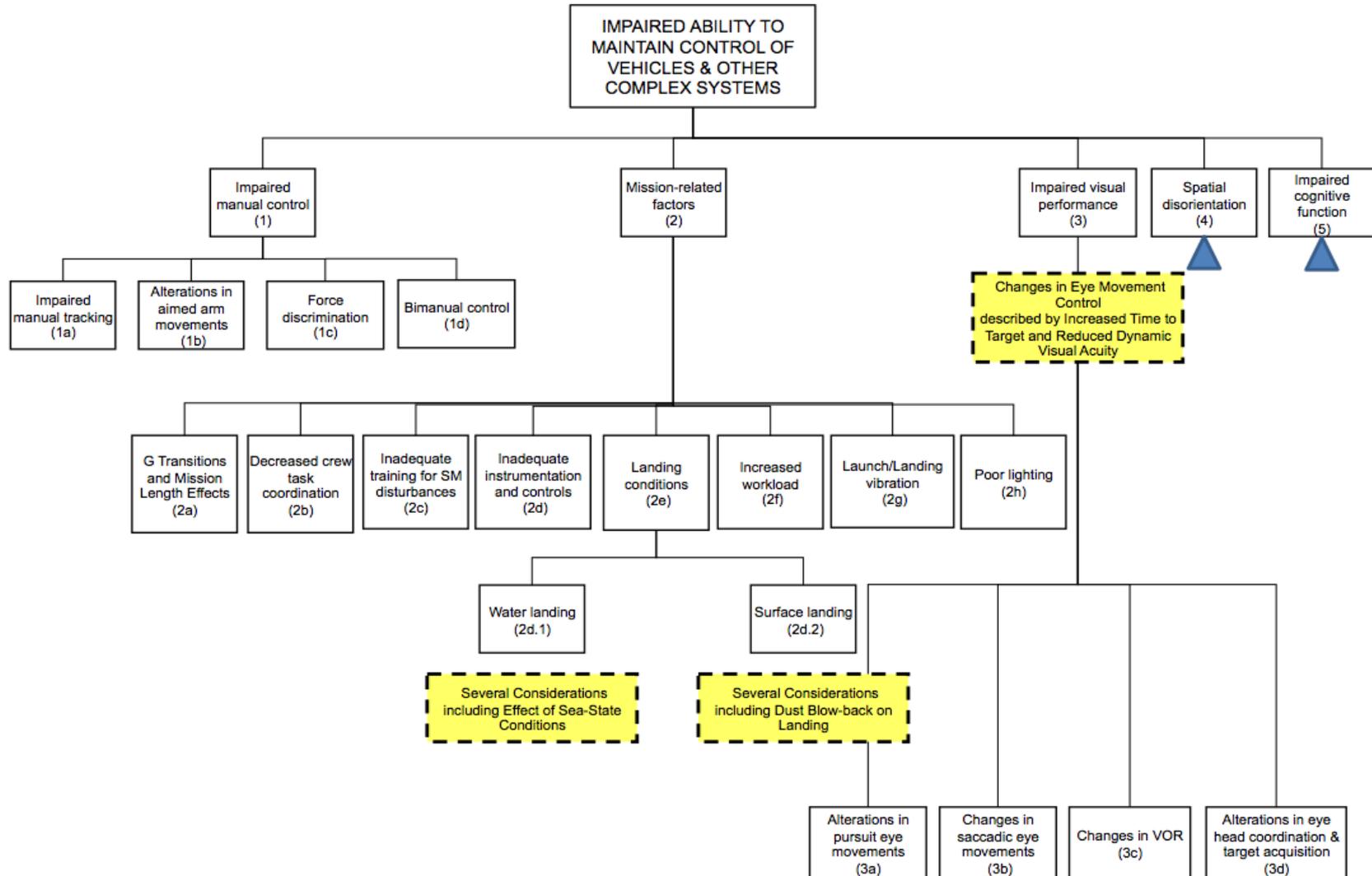




Master Logic Diagram





Master Logic Diagram (cont.)

